4CCess DB# 71623

SEARCH REQUEST FORM

Scientific and Technical Information Center

Requester's Full Name: VU	Le	Examiner #: 7/5/2 Date: 7/23		
Art Unit: 263 Phone N	lumber 30 8 -666	Examiner #: 1512 Date: 7133 3 Serial Number: 09 649 501 alls Format Preferred (circle): PAPER DISK E-MAI		
Mail Box and Bldg/Room Location	:_6D40Resu	ilts Format Preferred (circle): PAPER DISK E-MAI		
If more than one search is submitted, please prioritize searches in order of need. ***********************************				
Please provide a detailed statement of the	search topic, and describe	as specifically as possible the subject matter to be searched.		
Include the elected species or structures, ke utility of the invention. Define any terms to known. Please attach a copy of the cover s	that may have a special me	yms, and registry numbers, and combine with the concept or aning. Give examples or relevant citations, authors, etc, if		
known. I least attach a copy of the cover s	neet, pertinent claims, and	austract.		
Title of Invention:				
Inventors (please provide full names): _				
• -				
Earliest Priority Filing Date:				
		 parent, child, divisional, or issued patent numbers) along with the		
appropriate serial number.				
•				
	115	5914982		
	\bigcirc	114982		
STAFF USE ONLY	**************************************	**************************************		
Searcher: Kej	NA Sequence (#)	STN		
Searcher Phone #: 305-4071	AA Sequence (#)	Dialog		
Searcher Location:	Structure (#)	Questel/Orbit		
Date Searcher Picked Up:	Bibliographic	Dr.Link		
Date Completed: 773	Litigation	Lexis/Nexis		
Searcher Prep & Review Time:	Fulltext	Sequence Systems		
Clerical Prep Time:	Patent Family	WWW/Internet		
1				

PTO-1590 (8-01)

Memorandum

To: Mr. Le

From: Kimberly Johnson

Date: 7/23/02

Re: Litigation search request 5,914,982

Attached please find the results of your litigation search. Please feel free to contact me if you have additional questions or concerns. Thank you and have a great day.

Kimberly Johnson TIS EIC 2600 305-4071

Ouery/Command: prt max legalall

1/1 PLUSPAT - @QUESTEL-ORBIT - image

PN - US5914982 A 19990622 [US5914982]

TI - (A) Method and apparatus for training linear equalizers in a PCM modem

PA - (A) ROCKWELL SEMICONDUCTOR SYS INC (US)

IN - (A) BJARNASON ELIAS (IS); JONSSON OLAFUR (IS); OLAFSSON SVERRIR (IS)

AP - US87431697 19970613 [1997US-0874316]

PR - US87431697 19970613 [1997US-0874316]

IC - (A) H04B-001/38 H04L-005/16

EC - H04L-025/03B1A7 H04L-025/49M5

ICO - T04L-025/03B11A1 T04L-025/03B13F1T3F T04L-025/03B15A3A

PCL - ORIGINAL (O): 375222000; CROSS-REFERENCE (X): 375231000 375233000 375295000

DT - Basic

CT - US4489416; US4995057; US5463661; US5661718; US5666378; US5677951; US5694423; US5706344; US5721772; US5732112; US5737389; US5737410; US5740242; US5761088; US5764694; US5809075

STG - (A) United States patent

AB - A pulse code modulation (PCM) modem system employs a relatively white training signal to optimize the adaptive filter coefficients in the receiver equalizers. During the training mode, any line coding or equivalent spectral shaping is disabled to provide a training signal sequence having a substantially even spectral content. The presence of DC within the training signal reduces the likelihood that the error function of the equalizers will settle at a local minimum. Following the training interval, the encoder enables the line coder to condition the digital input sequences, introduce DC nulls, and reduce the detrimental effects of baseline wander.

1/1 LGST - ©LEGSTAT

PN - US 5914982 [US5914982]

AP - US 874316/97 19970613 [1997US-0874316]

DT - US-P

ACT - 19970613 US/AE-A

APPLICATION DATA (PATENT) US 874316/97 19970613 [1997US-0874316]

19971201 US/AS02

ASSIGNMENT OF ASSIGNOR'S INTEREST

ROCKWELL INTERNATIONAL CORPORATION 600 ANTON BOULEVARD, SUITE 700 COSTA MESA, CA * BJARNASON, ELIAS: 19971117; JONSSON, OLAFUR: 19971117; OLAFSSON, SVERRIR: 19971117

19990622 US/A PATENT 20020409 US/RF REISSUE APPLICATION FILED 20010504

UP - 2002-17

1/1 CRXX - @CLAIMS/RRX

PN - 5,914,982 A 19990622 [US5914982]

PA - Rockwell Semiconductor Systems Inc

ACT - 19991129 REASSIGNED

ASSIGNMENT OF ASSIGNOR'S INTEREST

Assignor: BJARNASON, ELIAS DATE SIGNED: 11/17/1997 JONSSON, OLAFUR DATE SIGNED: 11/17/1997

Assignee: ROCKWELL SEMICONDUCTOR SYSTEMS, INC. 4311 JAMBOREE ROAD NEWPORT BEACH, CALIFORNIA 92660-309

WE WI ORT BEHIOTI, OFFER ORGANIS

Reel 010456/Frame 0934

Contact: SNELL & WILMER MARK M. TAKAHASHI, ESQ ONE ARIZONA CENTER 400 EAST VAN BUREN STREET PHOENIX, AZ 85004-2202

20010504 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20020409
REISSUE REQUEST NUMBER: 09/849501
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2631

Reissue Patent Number:

1/1 PAST - ©Thomson Derwent

AN - 200215-001678

PN - 5914982 A [US5914982]

OG - 2002-04-09

ACT - REISSUE APPLICATION FILED

LEVEL 1 - 1 OF 1 PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

5914982

LEXIS-NEXIS
Library: PATENT
File: ALL

<=1> GET 1st DRAWING SHEET OF 3

June 22, 1999

Method and apparatus for training linear equalizers in a PCM modem

REISSUE: May 4, 2001 - Reissue Application filed May 4, 2001 (O.G. Apr. 9, 2002)

Ex. Gp.: 2631; Re. S.N. 09/849,501April 9, 2002

APPL-NO: 08874316

FILED-DATE: June 13, 1997

GRANTED-DATE: June 22, 1999

CORE TERMS: equalizer, training, modem, sequence, spectral, feedback, encoder,

feed-forward, adaptive, linear ...

ENGLISH-ABST:

A pulse code modulation (PCM) modem system employs a relatively white training signal to optimize the adaptive filter coefficients in the receiver equalizers. During the training mode, any line coding or equivalent spectral shaping is disabled to provide a training signal sequence having a substantially even spectral content. The presence of DC within the training signal reduces the likelihood that the error function of the equalizers will settle at a local minimum. Following the training interval, the encoder enables the line coder to condition the digital input sequences, introduce DC nulls, and reduce the detrimental effects of baseline wander.

5,914,982 OR 5914982

LEXIS-NEXIS
Library: PATENT
File: CASES

Your search request has found no CASES.

To edit the above request, use the arrow keys. Be sure to move the cursor to the end of the request before you enter it.

To enter a new search request, type it and press the ENTER key.

What you enter will be Search Level 1.

For further explanation, press the H key (for HELP) and then the ENTER key.

5,914,982 OR 5914982

Library: PATENT

File: JNLS

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5,914,982 OR 5914982

LEXIS-NEXIS
Library: NEWS
File: CURNWS

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To enter a new search request, type it and press the ${\tt ENTER}$ key.

What you enter will be Search Level 1.

For further explanation, press the H key (for HELP) and then the ENTER key.

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File 345:Inpadoc/Fam.& Legal Stat 1968-2002/UD=200228
      (c) 2002 EPO
     Set Items Description
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         ____
?s pn=us 5914982
     S1
           1 PN=US 5914982
?t 1/39/1
1/39/1
DIALOG(R) File 345: Inpadoc/Fam. & Legal Stat
(c) 2002 EPO. All rts. reserv.
15196959
Basic Patent (No, Kind, Date): US 5914982 A 19990622 <No. of Patents: 002>
Patent Family:
                               Applic No Kind Date
   Patent No
                Kind Date
                                              А
                                   US 874316
                      19990622
                                                       19970613
                                                                 (BASIC)
   US 5914982
                  Α
                                   US 213961
                                                  Α
                                                       19981217
   US 5949819
                   Α
                       19990907
Priority Data (No, Kind, Date):
   US 874316 A 19970613
   US 213961 A 19981217
   US 874316 Al 19970613
PATENT FAMILY:
UNITED STATES OF AMERICA (US)
  Patent (No, Kind, Date): US 5914982 A 19990622
   METHOD AND APPARATUS FOR TRAINING LINEAR EQUALIZERS IN A PCM MODEM
   Patent Assignee: ROCKWELL SEMICONDUCTOR SYS INC (US)
   Author (Inventor): BJARNASON ELIAS (IS); JONSSON OLAFUR (IS);
     OLAFSSON SVERRIR (IS)
   Priority (No, Kind, Date): US 874316 A
                                           19970613
   Applic (No, Kind, Date): US 874316 A 19970613
   National Class: * 375222000; 375231000; 375233000; 375295000
   IPC: * H04B-001/38; H04L-005/16
   Derwent WPI Acc No: * G 99-428601; G 99-579344; G 99-428601
   Language of Document: English
  Patent (No, Kind, Date): US 5949819 A
                                       19990907
   METHOD AND APPARATUS FOR TRAINING LINEAR EQUALIZERS IN A PCM MODEM
      (English)
   Patent Assignee: CONEXANT SYSTEMS INC (US)
   Author (Inventor): BJARNASON ELIAS (IS); JONSSON OLAFUR (IS);
     OLAFSSON SVERRIR (IS)
   Priority (No, Kind, Date): US 213961 A
                                          19981217; US 874316 A1
     19970613
   Applic (No, Kind, Date): US 213961 A
                                          19981217
   National Class: * 375222000; 375231000
   IPC: * H04B-001/38
   Derwent WPI Acc No: * G 99-428601; G 99-579344; G 99-579344
   Language of Document: English
UNITED STATES OF AMERICA (US)
 Legal Status (No, Type, Date, Code, Text):
                                              APPLICATION DATA (PATENT)
   US 5914982
                   Р
                       19970613 US AE
                             (APPL. DATA (PATENT))
                             US 874316 A 19970613
                                             ASSIGNMENT OF ASSIGNOR'S
                       19971201
                                US AS02
                   P
   US 5914982
                             INTEREST
                             ROCKWELL INTERNATIONAL CORPORATION 600 ANTON
                             BOULEVARD, SUITE 700 COSTA MESA, CA;
                             BJARNASON, ELIAS: 19971117; JONSSON, OLAFUR
                             : 19971117; OLAFSSON, SVERRIR : 19971117
                       19990622 US A
20020409 US RF
                                              PATENT
                   Ρ
   US 5914982
                   Ρ
                                              REISSUE APPLICATION FILED
   US 5914982
                             (REISSUE APPL. FILED)
                             20010504
                       19970613 US AA
                                              PRIORITY
                   Ρ
   US 5949819
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, , , u	S 5949819	P	US 874316 A1 19970613 19981217 US AE APPLICATION DATA (PATENT) (APPL. DATA (PATENT))
			US 213961 A 19981217
U	S 5949819	P	19990329 US ASO2 ASSIGNMENT OF ASSIGNOR'S INTEREST CONEXANT SYSTEMS, INC. 4311 JAMBOREE ROAD NEWPORT BEACH, CALIFORNIA 92660; BJARNASON, ELIAS: 19990316; JONSSON, OLAFUR: 19990316;
		_	OLAFSSON, SVERRIR: 19990316
U	S 5949819	P	19990907 US A PATENT

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